

Stylitis-41/Stylitis-101 comparison and features table.

Inputs	Styl-41	Styl-101
Total number of Analog inputs	4	18
Number of single-ended 12-bit Analog voltage/vane/PT100 inputs. Gain= 1	2	—
Number of single-ended 12-bit Analog voltage/current/vane/PT100 inputs. Resistor-set gain=1~1000	2	—
Number of single-ended 12-bit Analog voltage/current/vane/PT100 inputs. Program. gain=1,10,100,1000	—	0~6
Number of differential 12-bit Analog voltage/current/vane/PT100 inputs. Program. gain=1,10,100,1000	—	0~12
Number of 12-bit Analog 6-wire bridge inputs. Auto-zeroing. Programmable gain=1,10,100,1000	—	0~6
Number of 16-bit Counter inputs. Frequency, event, anemometer input configuration.	3	3~6
Number of Digital TTL inputs	—	3
Outputs		
Number of pulsed/fixed precision 5V outputs for sensor excitation.	2/3	—
Number of optional pulsed/fixed 12/18V outputs for current-loop (4~20mA transmitter) excitation	1	—
Number of pulsed/fixed precision programmable 0~5V/0~5mA outputs for sensor excitation. Step 0.1.	—	0~6
Number of programmable TTL outputs	—	0~3
Number of fixed auxiliary 5V outputs	2	2
Processing/Storage		
Individual slope/offset for each input	•	•
1 Hz sampling and 1, 5, 10, 15 or 60 minute statistical interval (min, max, average, standard dev.)	•	•
Variable 1~32Hz sampling and recording. Sampling rate individual for each channel.	—	•
Internal SRAM buffer.	128KB	512KB
Removable PCMCIA Flash Card Storage.	2MB	2~192MB
Connectivity		
Up to 4 units synchronized	NO	•
RS232 port for programming/data transfer	•	•
Standard modem support	•	•
GSM (cellular) modem support	•	•
LAN support	•	•
Power requirements		
2x9V Alkaline cell operation (typical)	2 Months	3 weeks
6~15V External power supply operation	•	•
General		
Integral LCD, keyboard and PCMCIA drive	•	•
Transient over-voltage protection for all inputs	•	•
Removable screw-terminals	•	•
IP65 environmental protection and -30 ~ +70°C operation	•	•